

ELECTRONIC SECURITY COMPONENT

Abstract of the Disclosure

In an electronic component including a two-way bus through which data elements travel between peripherals and a central processing unit at the rate
5 of a clock signal, the central processing unit and at least one of the peripherals each includes a data encryption/decryption cell. Each data encryption/decryption cell uses the same secret key. The secret
10 key is produced locally at each clock cycle in each cell from a random signal synchronous with the clock signal, and is applied to each of the cells by a one-way transmission line.